# **SMT Power Inductor**

### Military/Aerospace Grade





**Height:** 7.1mm Max

**Footprint:** 14.6mm x 12.6mm Max

**Qurrent Rating:** up to 15A

**MSL:** 1

📭 Frequency Range: 50kHz to 500kHz

Finish is Tin/Lead (Sn63/Pb37)

**Storage Temperature:** -55°C to +130°C

Max Reflow Temperature: 235°C

Electrical Specifications @ 25°C — Operating Temperature -40°C to 130°C								
Part Number	Inductance @ Irated (µH ±15%)	<b>Irated</b> (ADC)	DCR (m $\Omega$ )		Inductance @ O ADC	Saturation Current		Heating Current
			TYP	MAX	(µH ±15%)	25°C	100°C	(A)
2-TURN (LOW-LOSS) SERIES								
PL2059	0.85	39	.38	.48	0.91	46	37	73

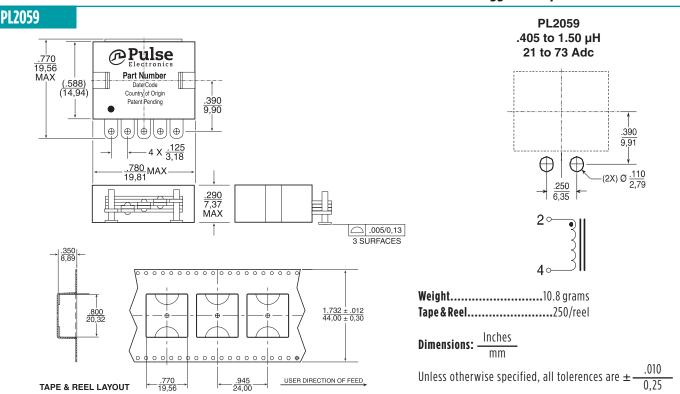
#### N otes:

- The rated current as listed is either 85% of the saturation current orthe heating current, depending on which value is lower.
- The saturation current is the current which causes the inductance todrop by 15% at the stated ambient temperatures (25°C and 100°C). This current is determined by placing the component in the specified ambient environment and applying a short duration pulse current (to eliminate self-heating effects) to the component.
- 3. The heating current is the DC current which causes the temperature of the part to increase by approximately 45°C. This current is determined by mounting the component on a PCB with .25" wide, 2 oz. equivalent copper traces, and applying the current to the device for 30 minutes with no forced air cooling.
- 4. In high volt\*time applications, additional heating in the component can occur due to core losses in the inductor which may necessitate derating the current in order to limit the temperature rise of the component.

- In order to determine the approximate total losses (or temperature rise) for a given application, the total copper and core losses should be taken into account. For approximate value of core losses, in a given application, use the core loss graph on page 24.
- Optional Tape & Reel packaging can be ordered by adding a "T" suffix to the part number (i.e. PL2059 becomes PL2059T). Pulse complies to industry standard tape and reel specification EIA481.
- 6. Meets solderability test per IPC/EIA J-STD-002B using flux type ORLO.
- 7. The temperature of the component (ambient plus temperature rise) must be within the stated operating temperature range.

### **Mechanical**

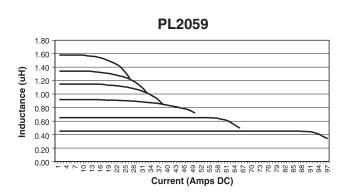
## **Suggested Layouts & Schematics**

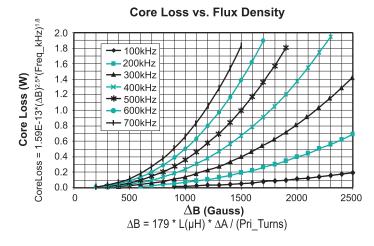


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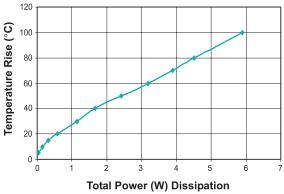


### **Inductance vs. Current Characteristics (25°C)**





# Temperature Rise vs. Power (W) Dissipation



Total Power Dissipation = Copper Loss (W) + Core Loss (W)

Copper Loss (W) = Current (rms) $^{2}$  \* DCR (m $\Omega$ ) / 1000 Core Loss (W) = per table

### For More Information

**Pulse Worldwide Pulse Europe Pulse China Headquarters Pulse North China Pulse South Asia Pulse North Asia** Headquarters Einsteinstrasse 1 B402, Shenzhen Academy of Room 2704/2705 135 Joo Seng Road 3F, No. 198 Two Pearl Buck Court Zhongyuan Road D-71083 Herrenberg Aerospace Technology Bldg. Super Ocean Finance Ctr. #03-02 10th Kejinan Road 2067 Yan An Road West PM Industrial Bldg. Zhongli City Bristol, PA 19007 Germany U.S.A. High-Tech Zone Shanghai 200336 Singapore 368363 Taoyuan County 320 Nanshan District Taiwan R. O. C. China Tel: 886 3 4356768 Shenzen, PR China 518057 Tel: 215 781 6400 Tel: 49 7032 78060 Tel: 86 755 33966678 Tel: 86 21 62787060 Tel: 65 6287 8998 Fax: 886 3 4356823 (Pulse) Fax: 49 7032 7806 135 Fax: 215 781 6403 Fax: 86 755 33966700 Fax: 86 2162786973 Fax: 65 6287 8998 Fax: 886 3 4356820 (FRE)

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